Cpld And Fpga Architecture Applications Previous Question Papers by Prometheus Books

**Cpld And Fpga Architecture Applications**

FPGAs and CPLDs are two of the well-known types of digital logic chips. When it comes to the internal architecture, the two chips are obviously different. On the other hand, CPLD (Complex Programmable Logic Device) is designed by using EEPROM (electrically erasable programmable read-only memory).

**Difference Between FPGA and CPLD**

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A field-programmable gate array (FPGA) is an integrated circuit designed to be configured by a customer or a designer after manufacturing – hence the term "field-programmable". The FPGA configuration is generally specified using a hardware description language (HDL), similar to that used for an Application-Specific Integrated Circuit (ASIC). Circuit diagrams were previously used to specify...

**Field-programmable gate array - Wikipedia**

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FPGA is an acronym for field programmable gate array—a semiconductor-integrated circuit where a large majority of the electrical functionality inside the device can be changed, even after the equipment has been shipped to customers out in the ‘field’.

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Un circuit logique programmable, ou réseau logique programmable, est un circuit intégré logique qui peut être reprogrammé après sa fabrication. Notons qu'il serait impropre de parler de programmation au sens logiciel (contrairement à un microprocesseur, il n'exécute aucune ligne de code).

**Circuit logique programmable — Wikipédia**

Lattice Semiconductor technical support answer database that includes technical solutions, frequently asked questions, and known Issues.

**Answer Database — Lattice Semiconductor**

A community for discussing topics related to all Xilinx products, as well as Xilinx software, intellectual property, applications and solutions.

**Xilinx — Home — Community Forums**

Prior to 2010, Xilinx offered two main FPGA families: the high-performance Virtex series and the high-volume Spartan series, with a cheaper EasyPath option for ramping to volume production. The company also provides two CPLD lines: the CoolRunner and the 9500 series. Each model series has been released in multiple generations since its launch. With the introduction of its 28 nm FPGAs in June ...
FPGA – Wikipedia
DM3730, DM3725 Digital Media Processors Check for Samples: DM3730,
DM3725 1 DM3730, DM3725 Digital Media Processors
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